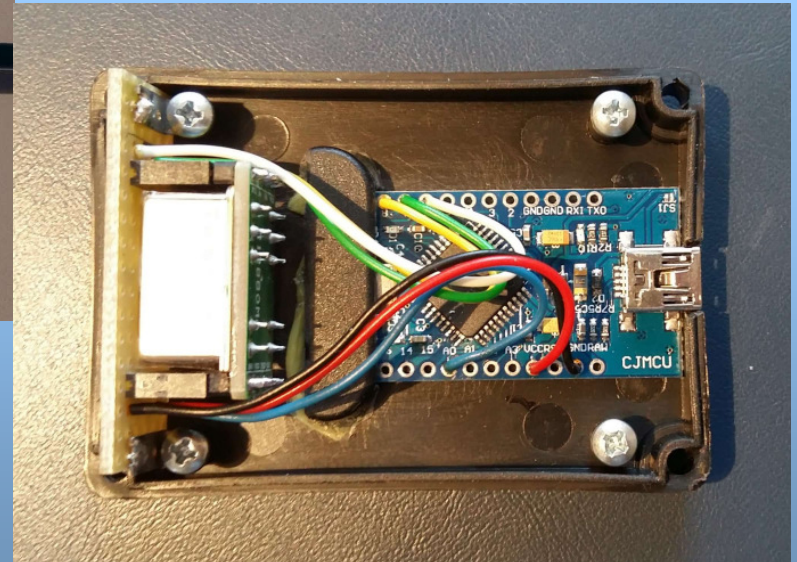


Spektrometer GH1 – C1288MA

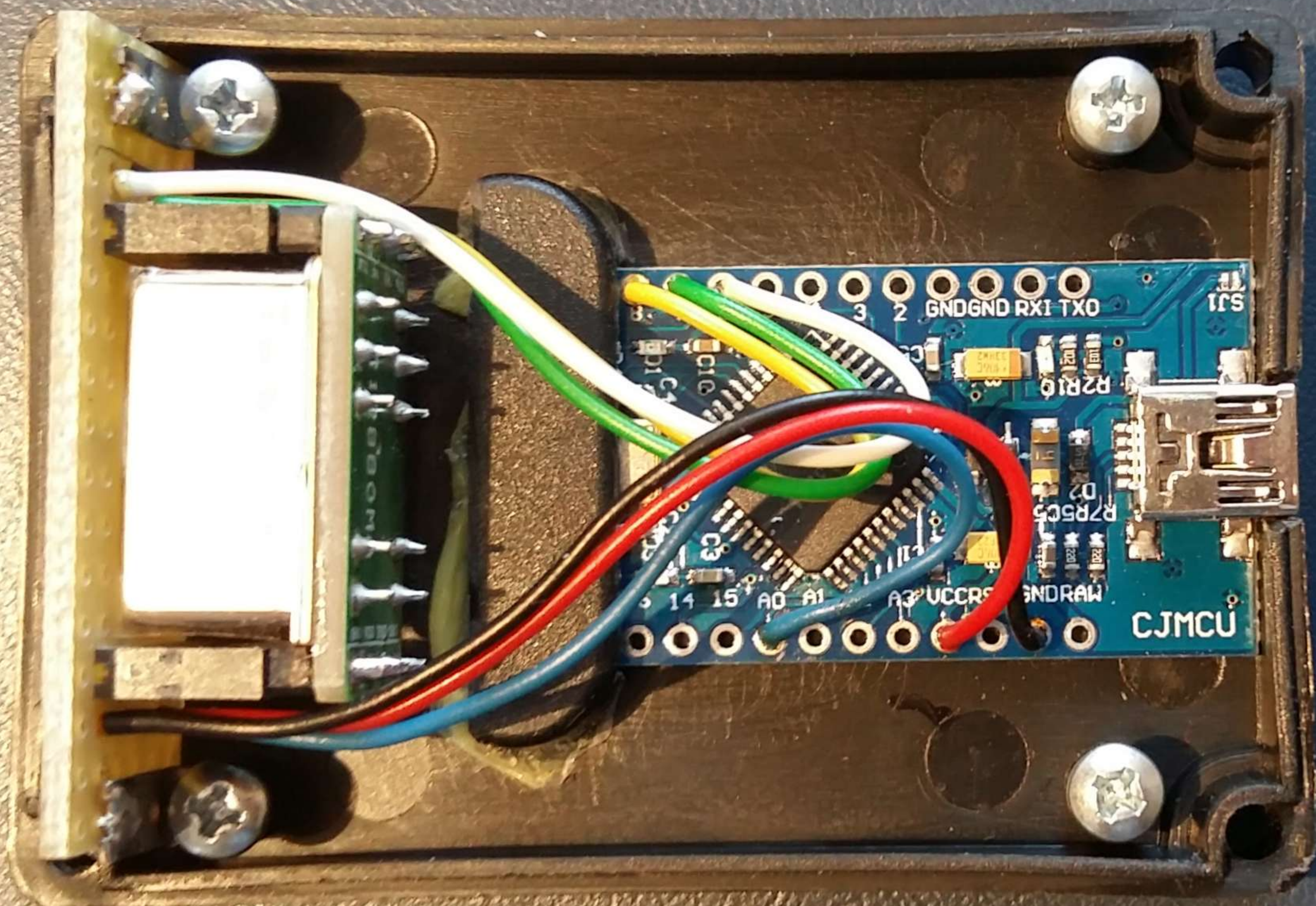
Gerd Heinz, 2/2017

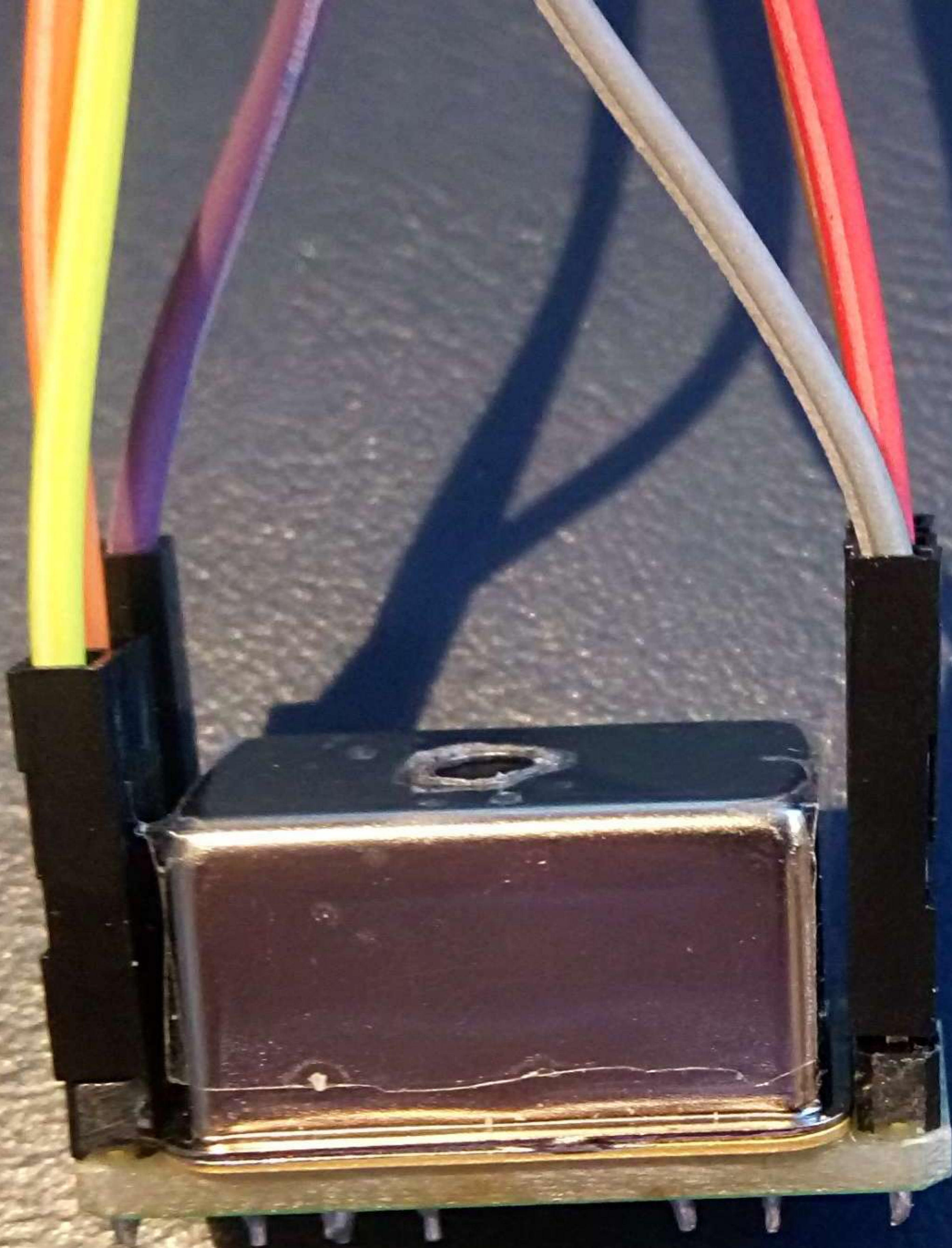
Dr. G. Heinz
www.gheinz.de
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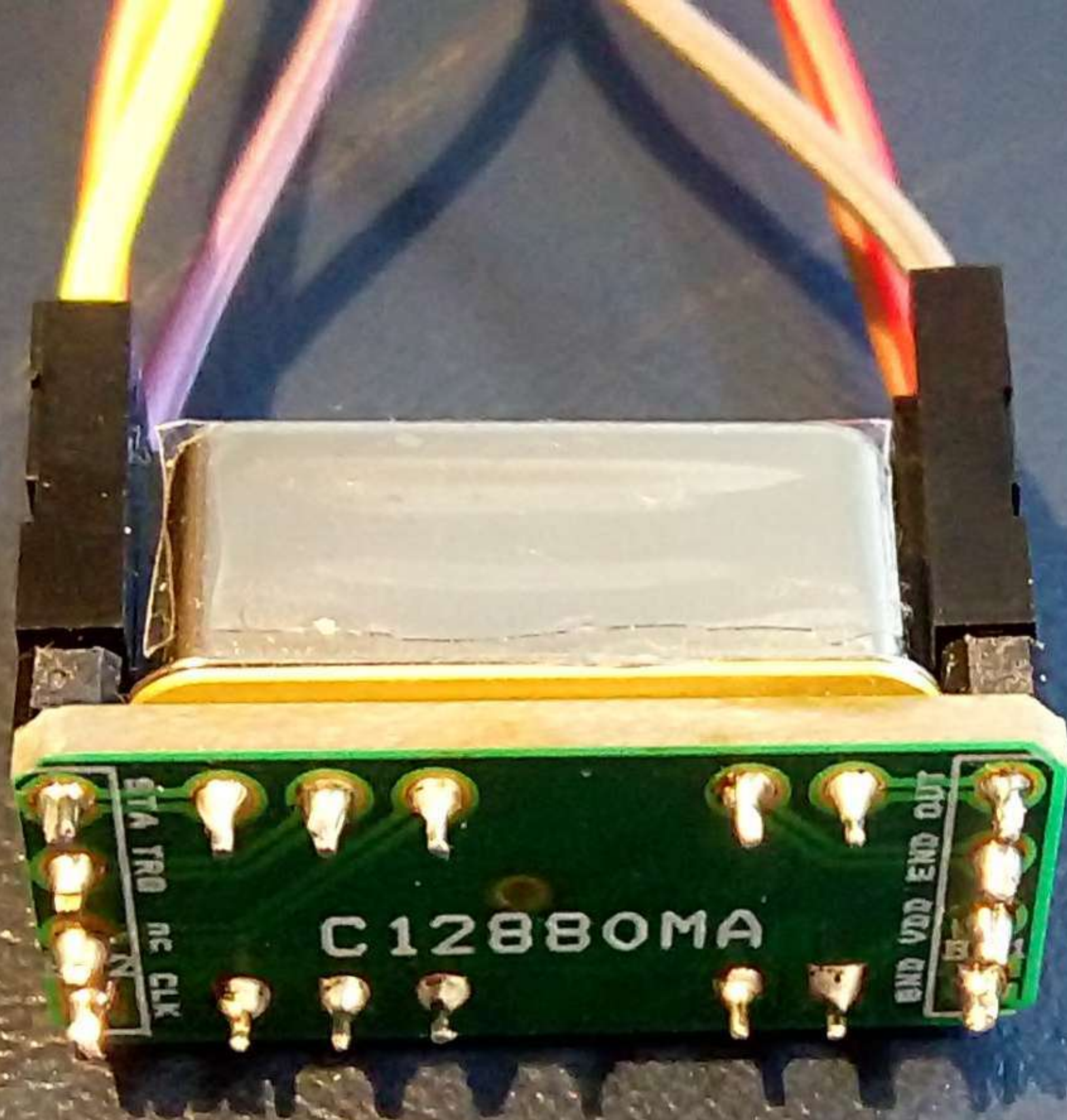


Kurzanleitung

- Spektrometer durch USB-Mini-Kabel mit PC verbinden
- "Automatisch nach neuem Treiber suchen" → Arduino Leonardo
- Windows installiert Treiber: "Neue Software kann jetzt verwendet werden"
- Software Spectrometer_C12880MA.exe auf PC starten; Varianten:
 - Win32 oder Win64
 - Linux32 oder Linux64
 - Linux-ARM64 oder Linux-ARMv6hf
- Spektrometer-Window erscheint
- Helligkeit einstellen (ADCmax sollte unter 1000 liegen)
- Für Hilfe irgendeine Taste drücken, z.B. 'h'
- Befehle
 - h help on/off
 - a moving average
 - t test-marker
 - s Skale anzeigen
 - d Debug (zur Processing3-Konsole)



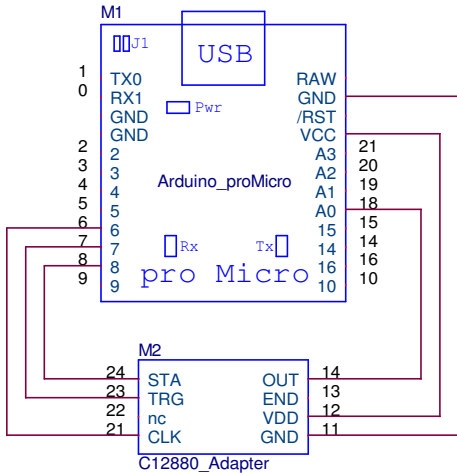


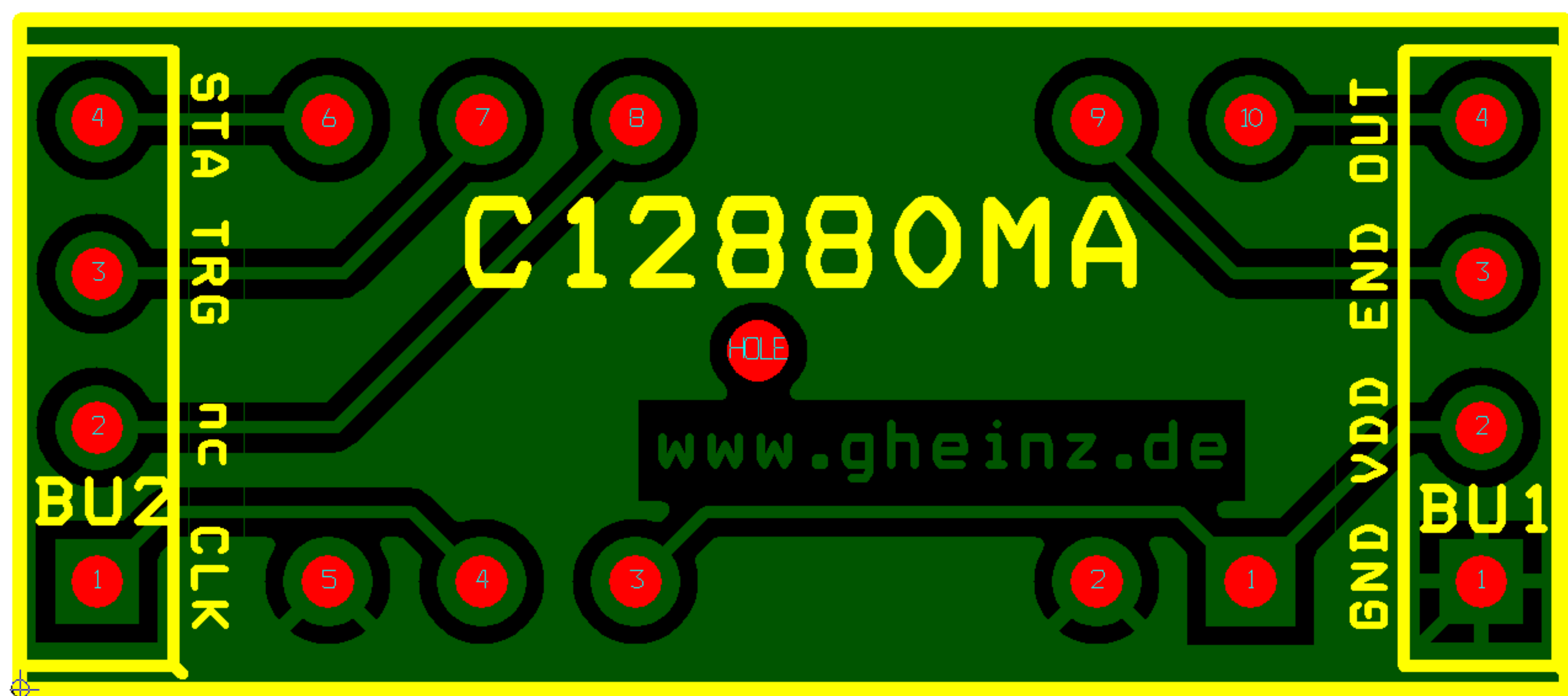


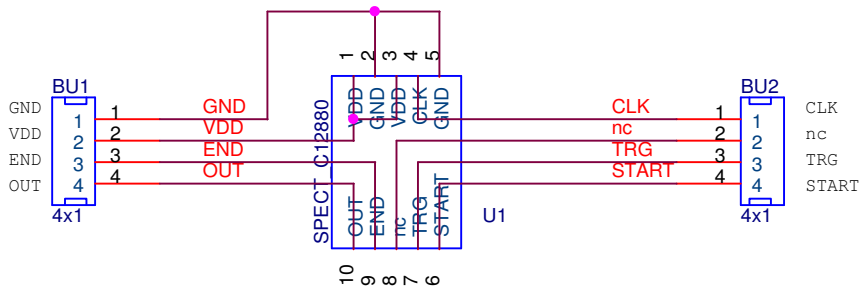
C12880MA

STA TRQ nc CLK

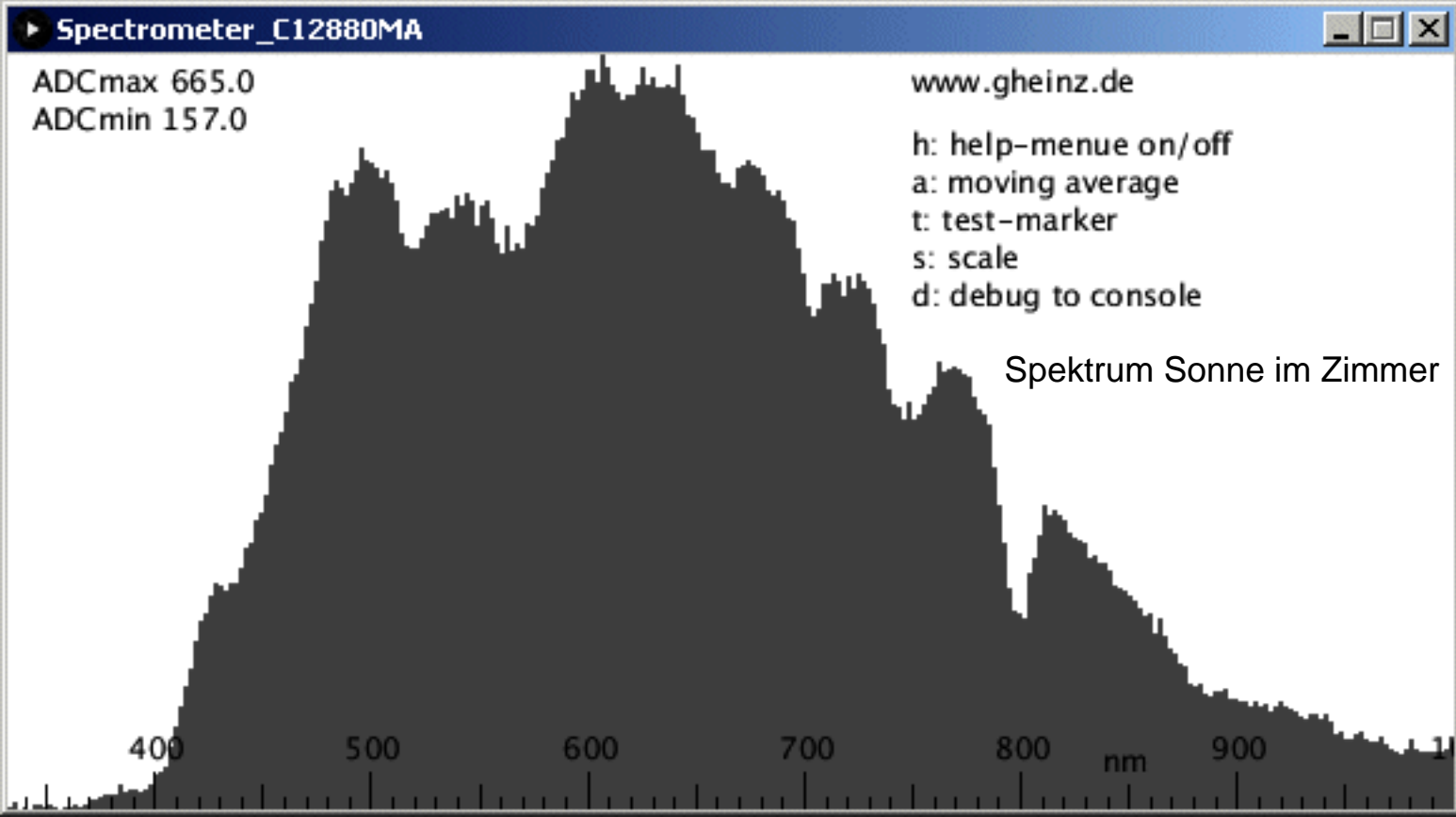
END VDD END OUT

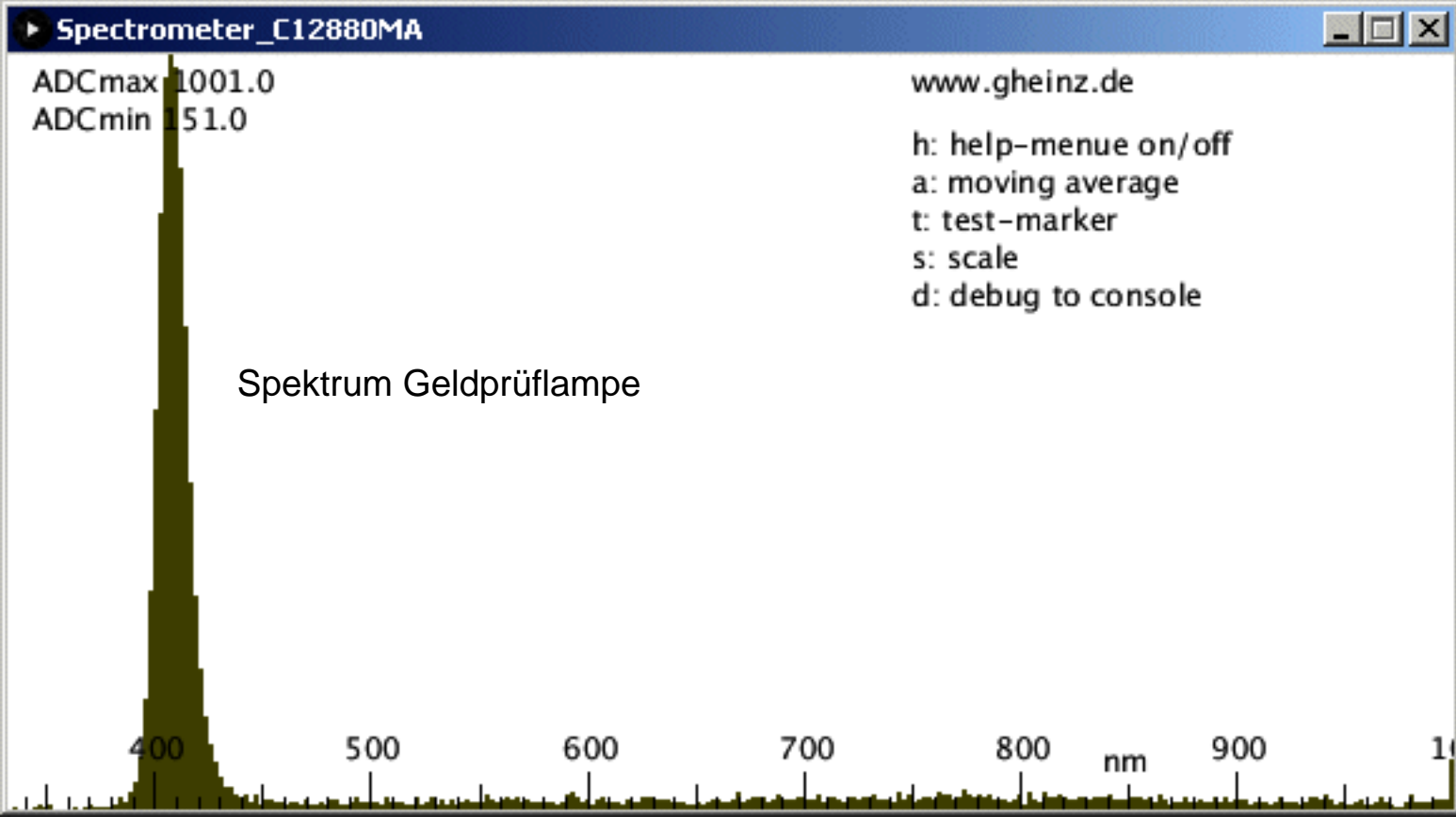


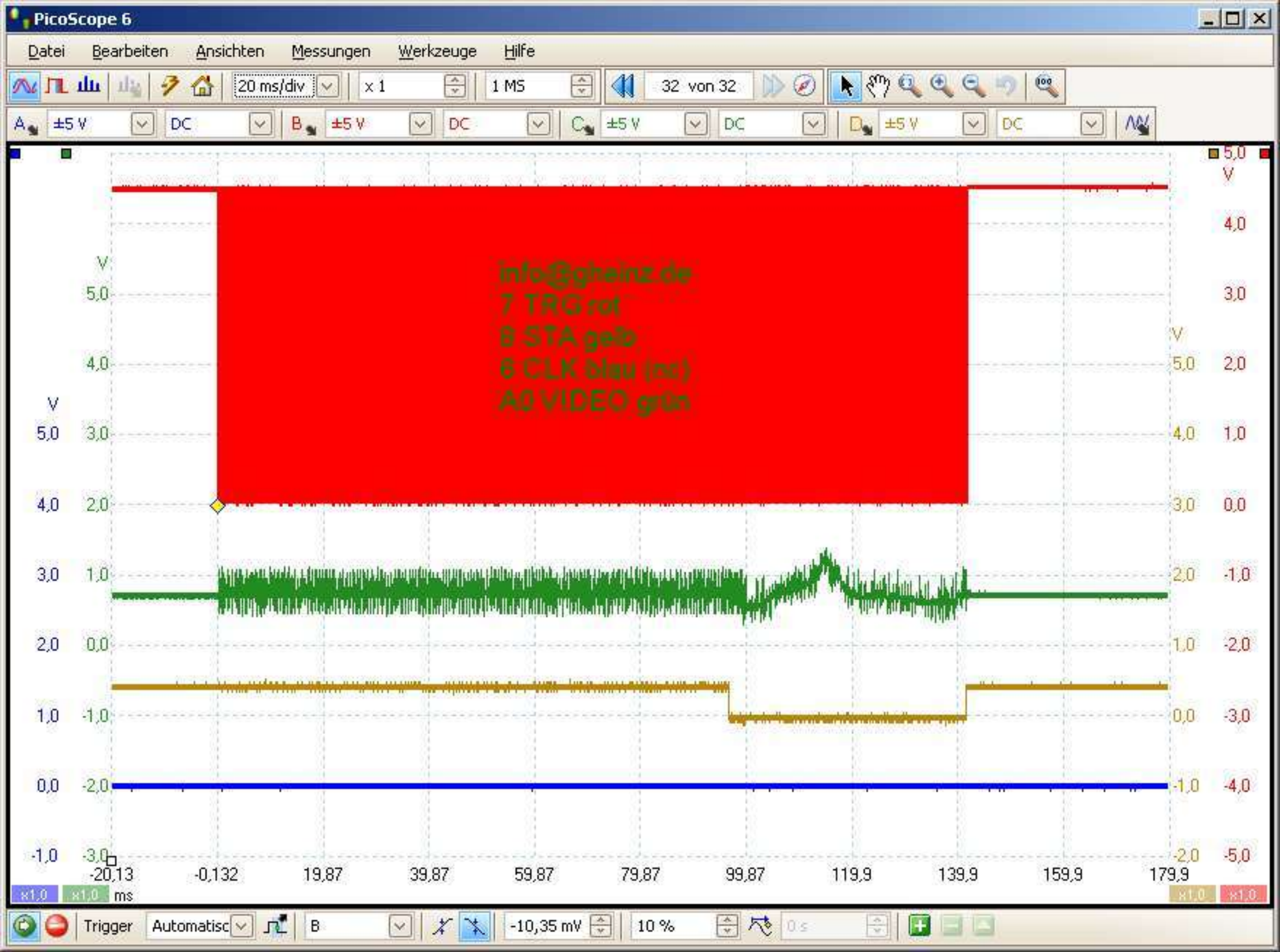




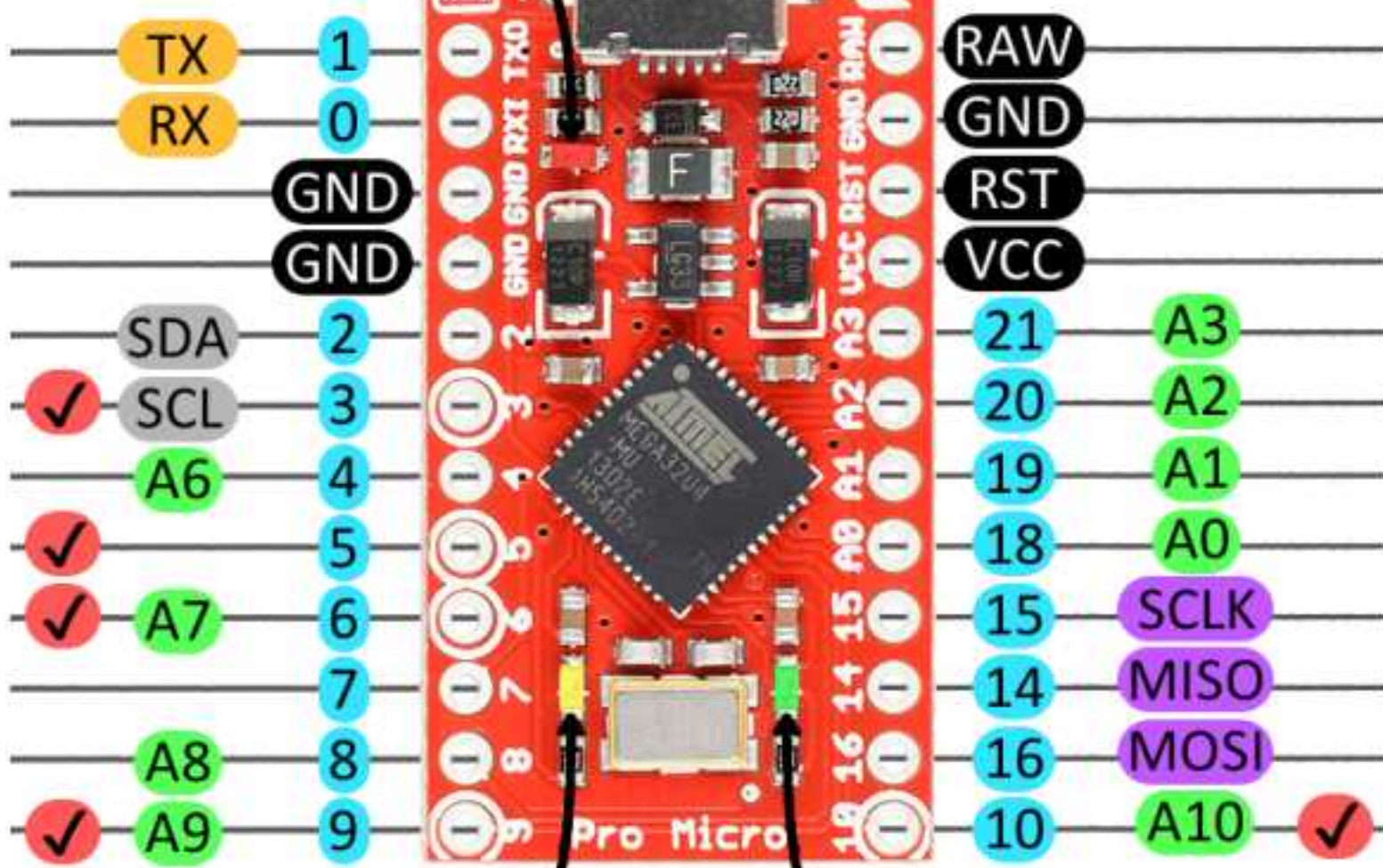
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Adapter für Microspectrometer Hamamatsu C12880MA			
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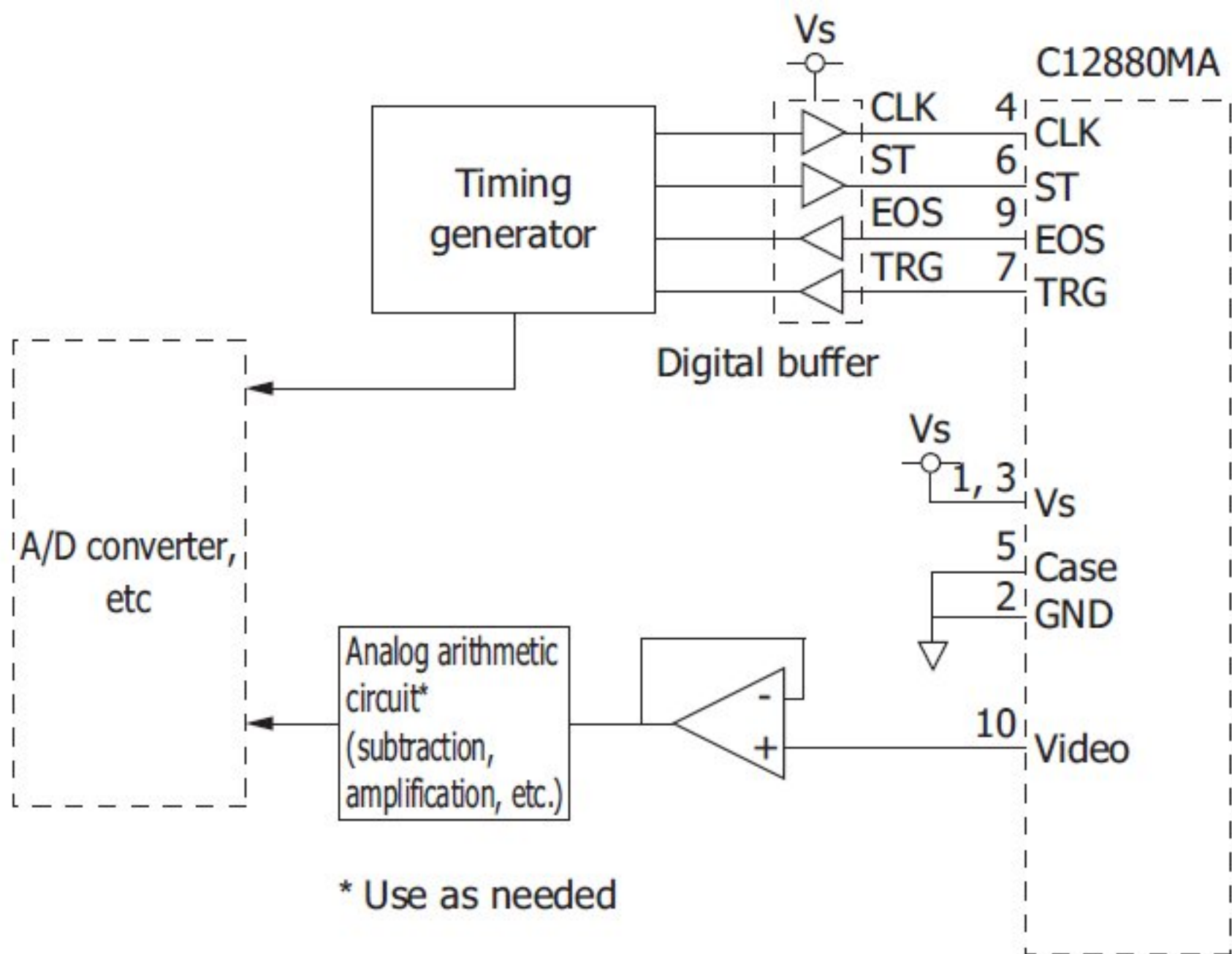
Power LED



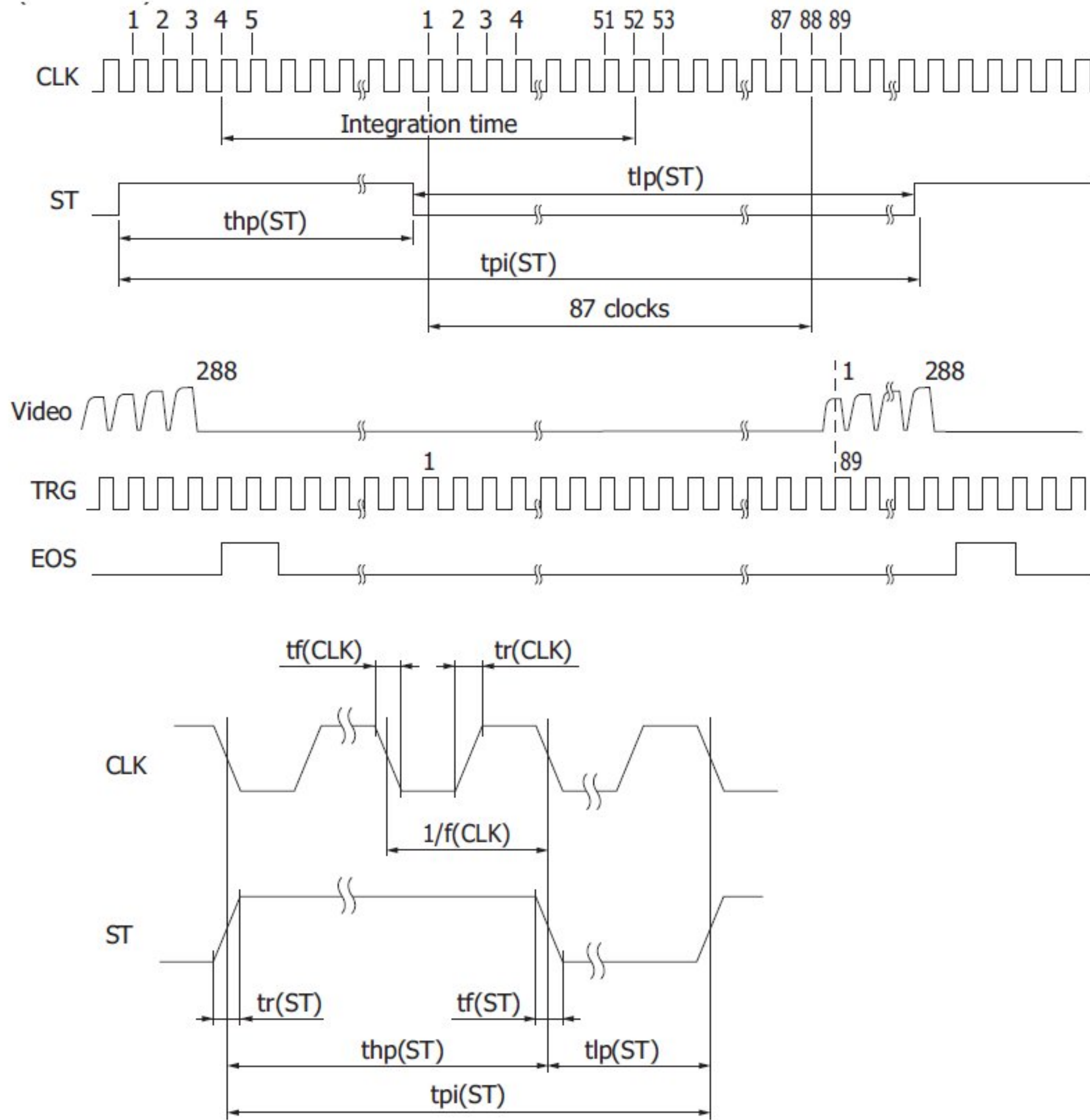
RX LED

TX LED

PWM Analog SPI I2C Serial Arduino Power



Timing chart



KACCC0771EA

Parameter	Symbol	Min.	Typ.	Max.	Unit
Start pulse cycle ^{*11}	$t_{pi}(ST)$	$381/f(CLK)$	-	-	s
Start pulse high period ^{*12}	$t_{hp}(ST)$	$6/f$	-	-	s
Start pulse low period	$t_{lp}(ST)$	$375/f$	-	-	s
Start pulse rise and fall times	$t_r(ST), t_f(ST)$	0	10	30	ns
Clock pulse duty	-	45	50	55	%
Clock pulse rise and fall times	$t_r(CLK), t_f(CLK)$	0	10	30	ns

*11: The shortest period required to output the video signals from all pixels.

*12: The integration time equals the high period of ST plus 48 CLK cycles.

The shift register starts operation at the rising edge of CLK immediately after ST goes low.

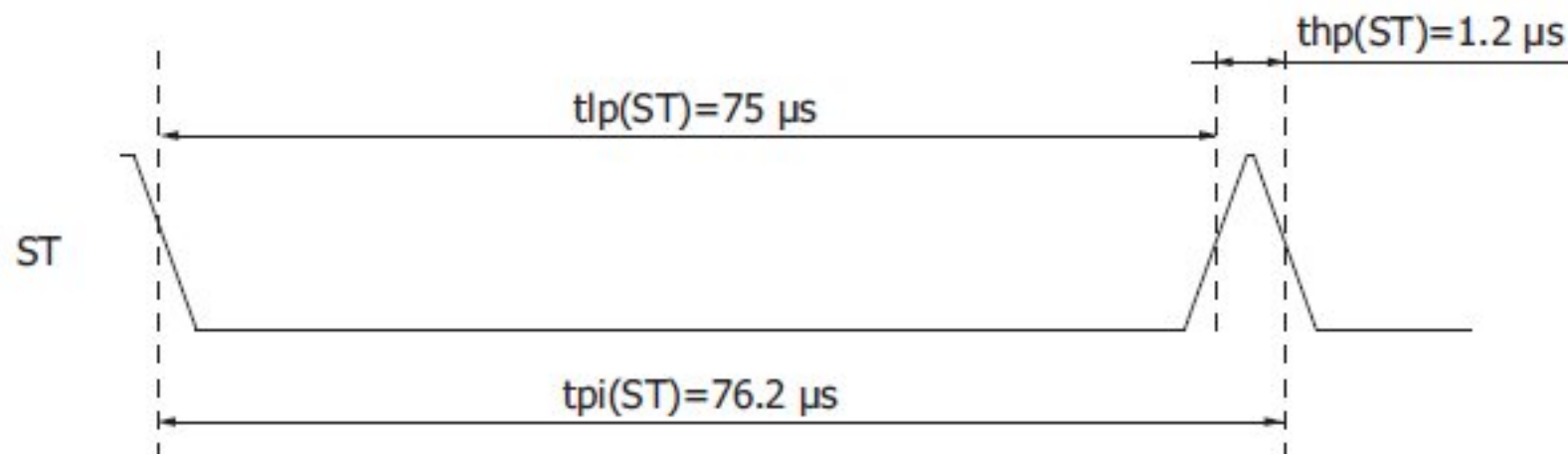
The integration time can be changed by changing the ratio of the high and low periods of ST.

If the first TRG pulse after ST goes low is counted as the first pulse, the Video signal should be acquired at the rising edge of the 89th TRG pulse.

❖ Operation example

This is an operating example when the clock pulse frequency is set to maximum (video data rate is also set to maximum), the time per scan to minimum, and the integration time to maximum.

- Clock pulse frequency $[f(\text{CLK})] = \text{Video data rate}$
 $= 5 \text{ MHz}$
- Start pulse cycle $[t_{pi}(\text{ST})] = 381/f(\text{CLK})$
 $= 381/5 \text{ MHz}$
 $= 76.2 \mu\text{s}$
- Low period of start pulse min. $[t_{lp}(\text{ST})] = 375/f(\text{CLK})$
 $= 375/5 \text{ MHz}$
 $= 75 \mu\text{s}$
- High period of start pulse $[t_{hp}(\text{ST})] = \text{Start pulse cycle } [t_{pi}(\text{ST})] - \text{Low period of start pulse min. } [t_{lp}(\text{ST})]$
 $= 76.2 \mu\text{s} - 75 \mu\text{s}$
 $= 1.2 \mu\text{s}$



KACCC0772EA

Integration time is equal to the high period of start pulse + 48 cycles of clock pulses, so it will be $1.2 \mu\text{s} + 9.6 \mu\text{s} = 10.8 \mu\text{s}$.


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2
3 UART-logfile
4 on Port COM87 115200 Baud, 1n8n
5
6 _____
7
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9
10 Port available: COM87
11
12 data.length was 288; form: i_data_hma;
13 data maximum 351.0; data minimum 118.0; delta 233.0; window height 300; scalefactor
14 1.2875537
15 Automatic Gain Control value sent to ProMicro: 0
16 ProMicros answer is:
17
18 [data]
19 Received: 0
20 new INTEGRTIME = 4
21
22 data.length was 288; form: i_data_hma;
23 data maximum 348.0; data minimum 118.0; delta 230.0; window height 300; scalefactor
24 1.3043479
25 Automatic Gain Control value sent to ProMicro: 0
26 ProMicros answer is:
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28 [data]
29 Received: 0
30 new INTEGRTIME = 4
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40 data.length was 288; form: i_data_hma;
41 moving average maximum = 0.80329996
42 AGC number of peaks = 1
43 AGC sent to ProMicro: +
44
45 incomming rest from ProMicro begin:
46
47 [data]
48 Received: +
49 new INTEGRTIME = 200
50
51 end incomming from ProMicro.
52
53 ...
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